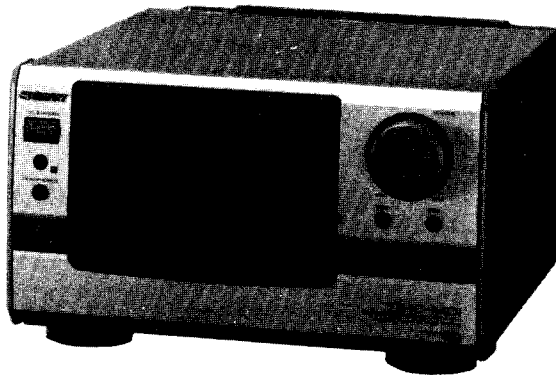


HCD-MD1

SERVICE MANUAL

*Canadian Model
AEP Model
Singapore Model*



HCD-MD1 is the Tuner, Amplifier,
Compact disc section in DHC-MD1.

CD SECTION	Model Name Using Similer Mechanism	NEW
	CD Mechanism Type	CDM13BC-5BD16
	Base Unit Type	BU-5BD16
	Optical Pick-up Type	KSS-390A

SPECIFICATIONS

CD player section

System	Compact disc digital audio system
Laser	Semiconductor laser
Wavelength	780 - 790 nm
Frequency response	2 Hz - 20 kHz (± 0.5 dB)
Signal-to-noise ratio	More than 90 dB
Outputs	DIGITAL OUT (Square optical connector jack, rear panel): wave length 660-nm

Tuner section

FM stereo, FM/AM superheterodyne tuner

FM tuner section

Tuning range	87.5 - 108.0 MHz
Antenna terminals	75 ohm unbalanced
Intermediate frequency	10.7 MHz

AM tuner section

Tuning range	
European models (except Germany and Italy):	
MW:	531 - 1,602 kHz
LW:	153 - 279 kHz
German model:	
AM:	531 - 1,602 kHz
Italian model:	
AM:	522 - 1,611 kHz

Canadian and Singapore model :

AM:	530 - 1,710 kHz (with the AM tuning interval set at 10 kHz) 531 - 1,602 kHz (with the AM tuning interval set at 9 kHz)
-----	---

Singapore model :

SW :	5.95 - 17.90 MHz
Antenna	AM loop antenna External antenna terminals
Intermediate frequency	450 kHz

Amplifier section

European models, Singapore model:

Continuous RMS power output:	
25 + 25 watts (6 ohms, at 1 kHz, DIN)	
30 + 30 watts (6 ohms, at 1 kHz, 5% THD)	

Music power output:	
45 + 45 watts (6 ohms, at 1 kHz, 10% THD)	

Canadian model:

Continuous RMS power output:	
22 + 22 watts (6 ohms, at 40 - 20,000 Hz, 0.9% THD)	
30 + 30 watts (6 ohms, at 1 kHz, 5% THD)	

Inputs

MIC (mini jack):	
Sensitivity	1 mV, impedance 600 ohms
European models:	
PHONO (phono jack):	
Sensitivity	3 mV, impedance 47 kilohms
AUX (phono jacks):	
Sensitivity	450 mV, impedance 47 kilohms
Canadian model, Singapore model:	
AUX 1 (phono jack):	
Sensitivity	450 mV, impedance 47 kilohms
AUX 2 (phono jacks):	
Sensitivity	450 mV, impedance 47 kilohms

Outputs

LINE OUT (phono jacks):	
Output level	250 mV (at 47 kilohms)
Load impedance over	10 kilohms
HEADPHONES (stereo mini jack):	
accepts headphones of 8 ohms or more.	
SPEAKER:	
accepts speakers of 6 to 16 ohms.	

General

Destination	Power requirements	Power consumption
Europe	220 - 230 V AC, 50/60 Hz	70 W
Canada	120 V AC, 60 Hz	70 W
Singapore	110 - 120 V, 220 - 240 V AC 50/60 Hz	70 W

Dimensions

Approx. 225 x 135 x 305 mm (w/h/d) (8 7/8 x 5 3/8 x 12 1/8 inches)

Mass Approx. 4.8 kg (10 lb 9 oz)

U.S. and foreign patents licensed from Dolby Laboratories Licensing Corporation.

Design and specifications subject to change without notice.



COMPACT DISC RECEIVER
SONY®

Laser component in this product is capable of emitting radiation exceeding the limit for Class 1.

CLASS 1 LASER PRODUCT
LUOKAN 1 LASERLAITE
KLASS 1 LASERAPPARAT

This appliance is classified as a CLASS 1 LASER product. The CLASS 1 LASER PRODUCT MARKING is located on the rear exterior.

CAUTION : INVISIBLE LASER RADIATION WHEN OPEN. AVOID EXPOSURE TO BEAM.
ADVARSEL : USYNLIG LASERSTRÅLING VED ÅBNING NÅR SIKKERHEDSAFBRYDERE ER UDE AF FUNKTION. UNDGÅ UDSÆTTELSE FOR STRÅLING.
VARO! : AVATTAESSA JA SUOJALUKUTUS OHJETETTAESSA DLET ALLTIINA LASERSATEILYLLE.
VARNING : LASERSTRÅLING NÅR DENNA DEL ÄR ÖPPNAD OCH SPÄRREN ÄR URÖPPPLAD.
ADVARSEL : USYNLIG LASERSTRÅLING NÅR DEKSEL ÅPNEK UNNGÅ EKSPONERING FOR STRÅLEN.

This caution label is located inside the unit.

Notes on chip component replacement

- Never reuse a disconnected chip component.
- Notice that the minus side of a tantalum capacitor may be damaged by heat.

Flexible Circuit Board Repairing

- Keep the temperature of the soldering iron around 270 °C during repairing.
- Do not touch the soldering iron on the same conductor of the circuit board (within 3 times).
- Be careful not to apply force on the conductor when soldering or unsoldering.

TABLE OF CONTENTS

Section	Title	Page
1.	SERVICING NOTE	3
2.	GENERAL	4
3.	DISASSEMBLY	
3-1.	Front Panel	5
3-2.	Back Panel	5
3-3.	Power Board	6
3-4.	Power Supply Board and Main Board	6
4.	ELECTRICAL ADJUSTMENTS	
	FM Section	7
	AM Section	7
	CD Section	9
	Test Mode	10
5.	DIAGRAMS	
5-1.	Circuit Boards location	11
5-2.	IC Pin Functions	
	• IC202 CD System Controller (μPD75116GF-J06-3BE)	12
	• IC301 Main System Controller (μPD78012BGC-607-AB8)	13
5-3.	Block Diagrams	
	Main Section	15
	CD Section	19
5-4.	Printed Wiring Boards — Tuner Section —	21
5-5.	Schematic Diagram — Tuner Section —	23
5-6.	Schematic Diagram — CD Section —	27
5-7.	Printed Wiring Boards — CD Section —	31
5-8.	Printed Wiring Boards — Main Section —	34
5-9.	Schematic Diagram — Main Section —	37
5-10.	Schematic Diagram — Panel/Power Section —	42
5-11.	Printed Wiring Boards — Panel/ Power Section —	47
5-12.	Semiconductor Lead Layouts	50
6.	EXPLODED VIEWS	
6-1.	Case and Chassis Section	52
6-2.	CD Mechanism Section (CDM13BC-5BD16)	54
6-3.	Base Unit Section (BU-5BD16)	55
7.	ELECTRICAL PARTS LIST	56

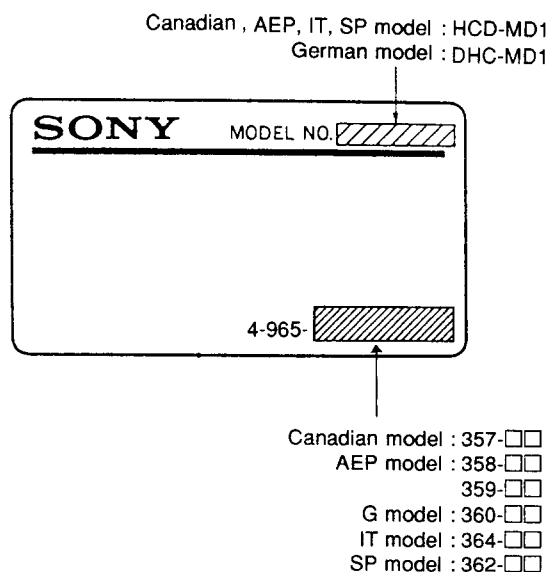
SAFETY-RELATED COMPONENT WARNING !!

COMPONENTS IDENTIFIED BY MARK Δ OR DOTTED LINE WITH MARK Δ ON THE SCHEMATIC DIAGRAMS AND IN THE PARTS LIST ARE CRITICAL TO SAFE OPERATION. REPLACE THESE COMPONENTS WITH SONY PARTS WHOSE PART NUMBERS APPEAR AS SHOWN IN THIS MANUAL OR IN SUPPLEMENTS PUBLISHED BY SONY.

ATTENTION AU COMPOSANT AYANT RAPPORT À LA SÉCURITÉ!!

LES COMPOSANTS IDENTIFIÉS PAR UNE MARQUE Δ SUR LES DIAGRAMMES SCHÉMATIQUES ET LA LISTE DES PIÈCES SONT CRITIQUES POUR LA SÉCURITÉ DE FONCTIONNEMENT. NE REMPLACER CES COMPOSANTS QUE PAR DES PIÈCES SONY DONT LES NUMÉROS SONT DONNÉS DANS CE MANUEL OU DANS LES SUPPLÉMENTS PUBLIÉS PAR SONY.

MODEL IDENTIFICATION
— SPECIFICATION LABEL —



Note

G : German model
IT : Italian model
SP : Singapore model

SECTION 1
SERVICING NOTE

**NOTES ON HANDLING THE OPTICAL PICK-UP
BLOCK OR BASE UNIT**

The laser diode in the optical pick-up block may suffer electrostatic break-down because of the potential difference generated by the charged electrostatic load, etc. on clothing and the human body.

During repair, pay attention to electrostatic break-down and also use the procedure in the printed matter which is included in the repair parts.

The flexible board is easily damaged and should be handled with care.

NOTES ON LASER DIODE EMISSION CHECK

The laser beam on this model is concentrated so as to be focused on the disc reflective surface by the objective lens in the optical pick-up block. Therefore, when checking the laser diode emission, observe from more than 30 cm away from the objective lens.

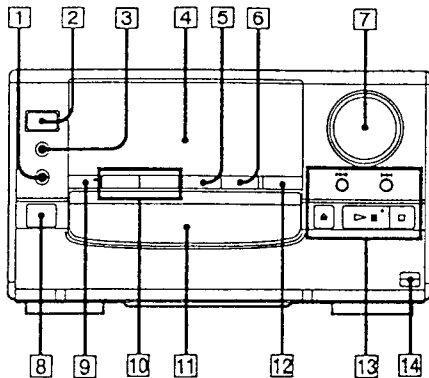
**LASER DIODE AND FOCUS SEARCH OPERATION
CHECK**

Carry out the "S curve check" in "CD section adjustment" and check that the S curve waveform is output three times.

SECTION 2 GENERAL

This section is extracted from instruction manual.

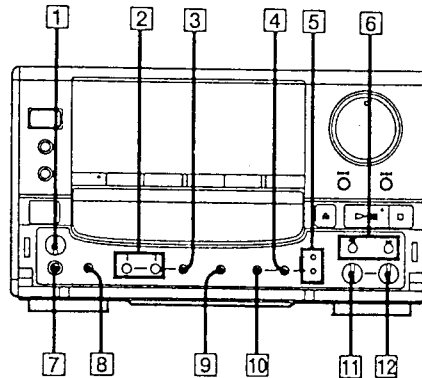
Front Panel



- 1 HEADPHONES jack (stereo minijack) (22, 26)
- 2 SYSTEM POWER switch (8)
- 3 Remote sensor
- 4 Display window (8)
- 5 EFFECT button (22)
- 6 EQ PRESET button (22)
- 7 VOLUME control (8, 22)
- 8 FUNCTION button (24)
- 9 BAND button (11)
- 10 -/+ buttons (11)
- 11 Disc tray
- 12 P.MODE (play mode) button (9)
- 13 CD player operating buttons
△ (open/close of the disc tray) (8)
▷|| (play/pause)
□ (stop)
◀▶▶▶ (AMS)*
- 14 Cover open/close button

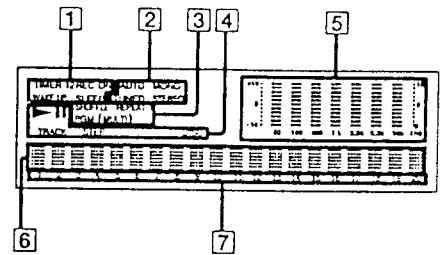
*AMS: Automatic Music Sensor

(Inside the cover)



- 1 MIC LEVEL control (24)
- 2 CLOCK SET buttons (6)
TIMER 1 button
TIMER 2 button
- 3 TIMER REC button (24)
- 4 TUNING MODE button (11)
- 5 TUNING/PRESET indicators (11)
- 6 ◀▶▶▶ (manual search) buttons (8)
- 7 MIC jack (24, 25)
- 8 KARAOKE PON button (25)
- 9 MEMORY/NEXT button (12, 23, 24)
- 10 STEREO/MONO button (11)
- 11 FREQUENCY control (22)
- 12 LEVEL control (22)

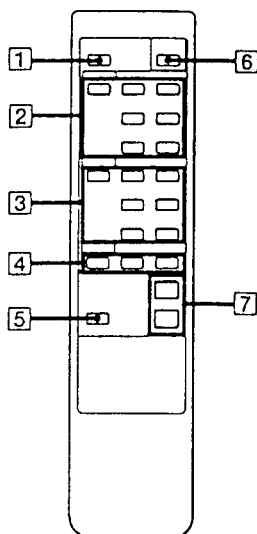
Display Window



- 1 Timer indication (23, 24)
- 2 Tuner indication (11)
- 3 CD play mode indication (9, 10)
- 4 CD play status indication (8)
- 5 Spectrum analyzer (8, 11, 22)
- 6 Timer/time/function/tuner/CD indications (6, 8, 11, 23)
- 7 Music calendar (8)

Remote commander

RM-S11MD

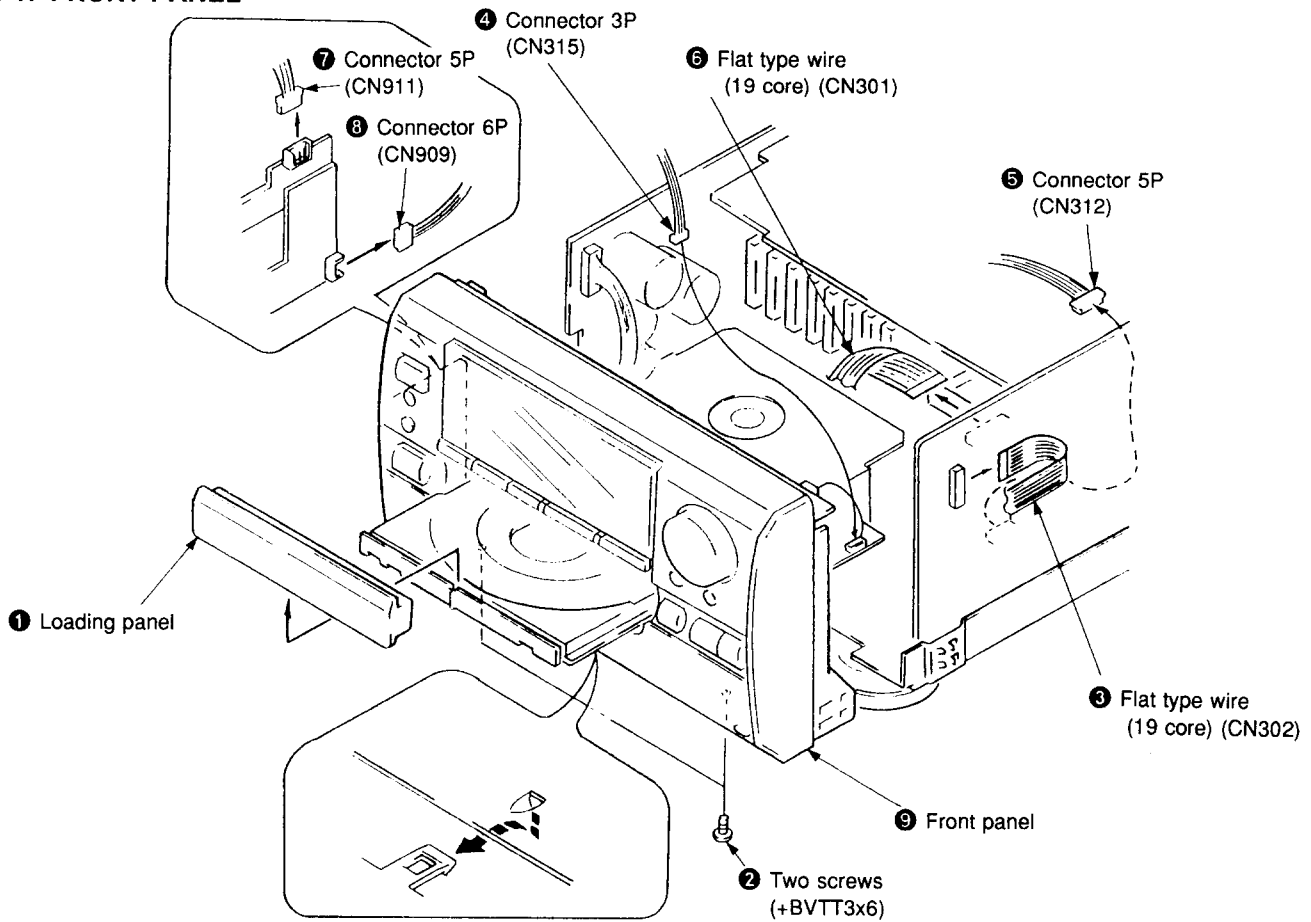


- 1 SLEEP button (23)
- 2 MD recorder operating buttons
▷ (play) (14)
|| (pause) (14)
■ (stop) (14)
◀▶▶▶ AMS* (14)
REC ● (recording) (17)
REPEAT button (15)
- 3 CD player operating buttons
▷ (play) (8)
|| (pause) (8)
■ (stop) (8)
◀▶▶▶ AMS* (8)
TIME button (9)
REPEAT button (10)
- 4 BAND button (11)
PRESET -/+ buttons (11)
- 5 FUNCTION button (24)
- 6 SYSTEM POWER button (8, 23)
- 7 VOL +/- buttons (22)

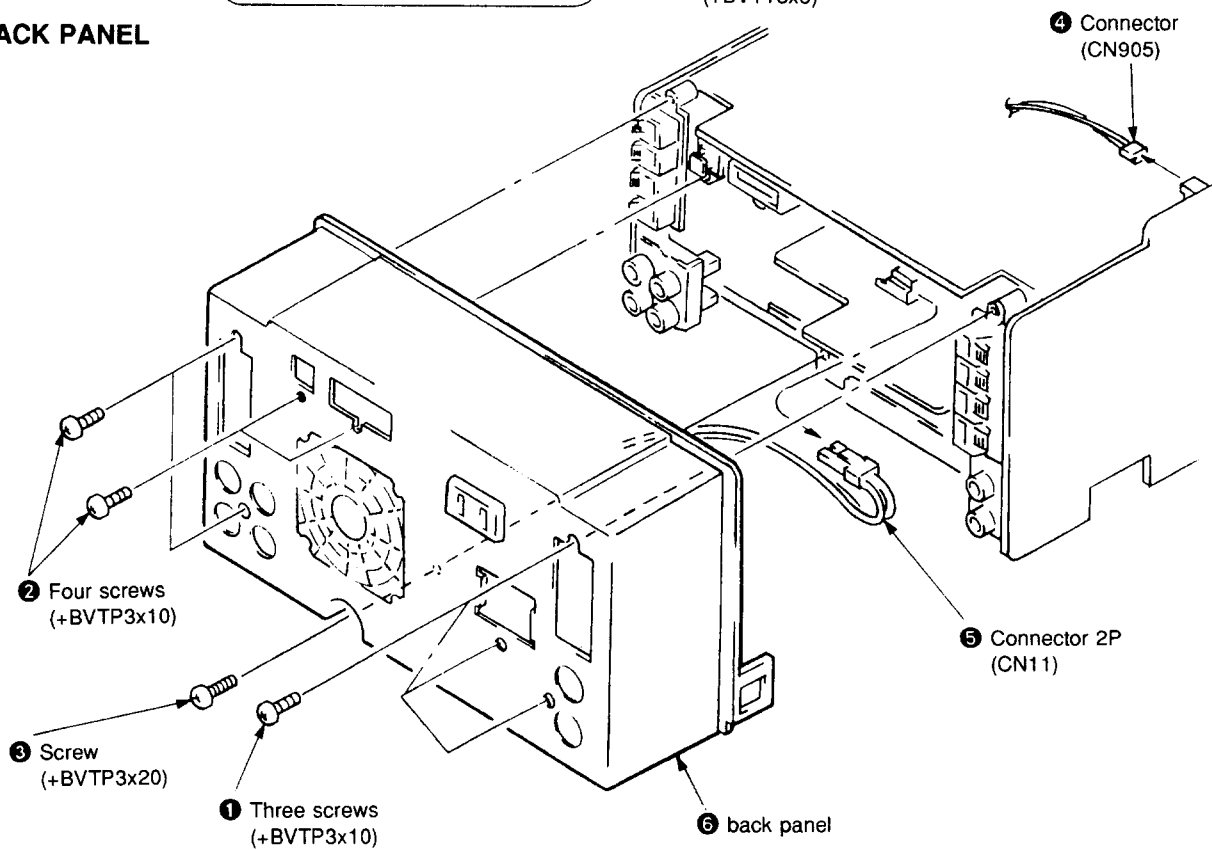
*AMS: Automatic Music Sensor

SECTION 3 DISASSEMBLY

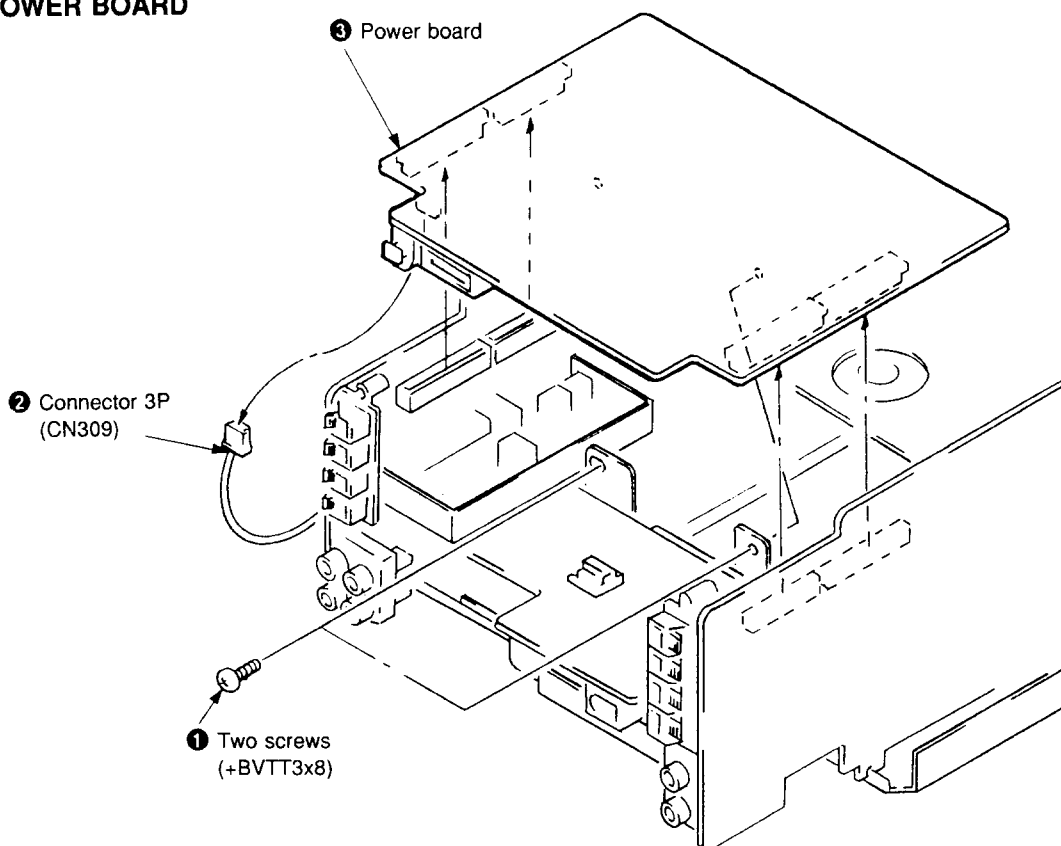
3-1. FRONT PANEL



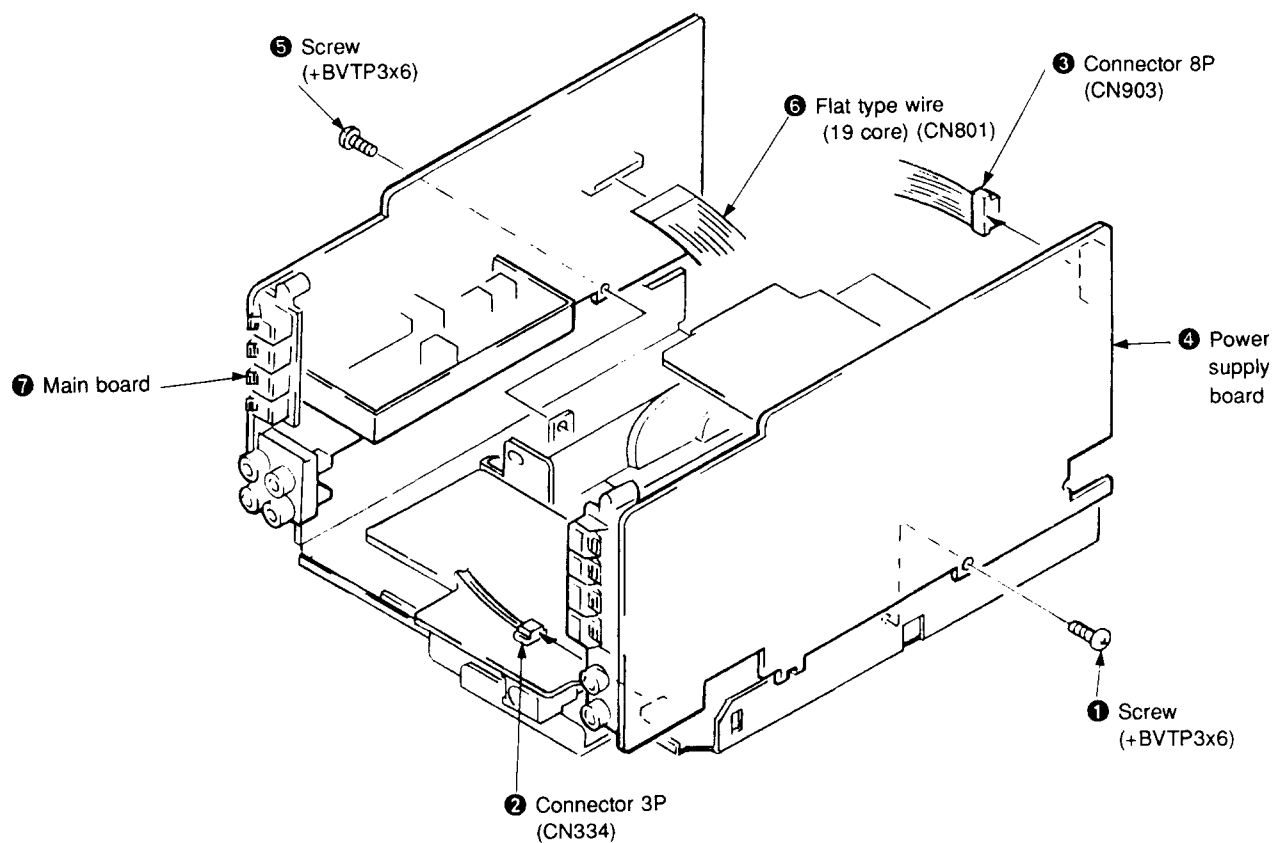
3-2. BACK PANEL



3-3. POWER BOARD



3-4. POWER SUPPLY BOARD AND MAIN BOARD



SECTION 4

ELECTRICAL ADJUSTMENTS

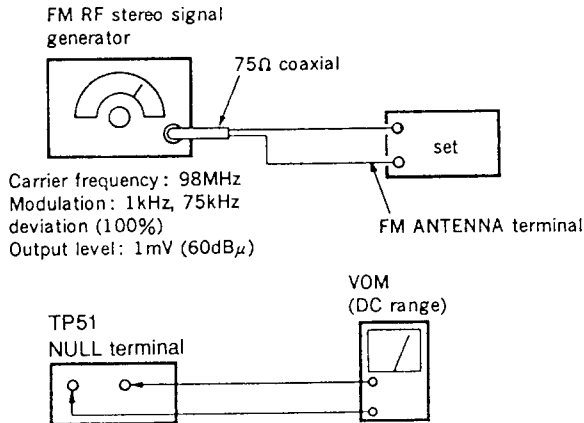
Precautions in Repairing

If the front end unit fails, it is difficult to repair the inner circuits, so replace the entire front end unit.

FM SECTION

FM Discriminator Adjustment (NULL Adjustment)

Setting :
BAND : FM



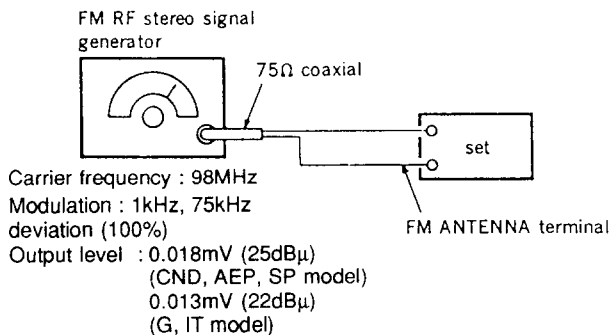
Procedure :

1. Tune the set to 98MHz.
2. Adjust IFT51 for 0Vdc reading on the VOM.

Note : FM Tuning Level adjustment should be made after FM discriminator alignment.

FM Tuned Indication Lighting Level

Setting :
BAND : FM



Procedure :

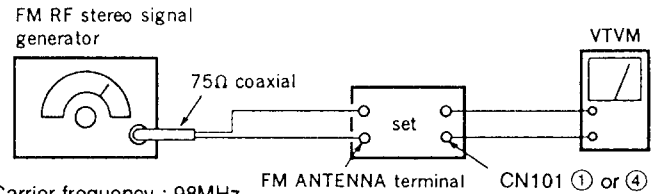
1. Tune the set to 98MHz.
2. Adjust RV52 so that the TUNED indicator goes on.

Note :

CND : Canadian model
IT : Italian model
G : German model
SP : Singapore model

FM Stereo Separation Adjustment

Setting :
BAND : FM



Procedure :

Tune the set to 98MHz.

FM stereo Signal generator output channel	VTVM connection	VTVM reading (dB)
L-CH	L-CH	Ⓐ
R-CH	L-CH	Ⓑ [Ⓐ] Adjust RV53 for minimum reading.
R-CH	R-CH	Ⓒ
L-CH	R-CH	Ⓓ [Ⓒ] Adjust RV53 for minimum reading.

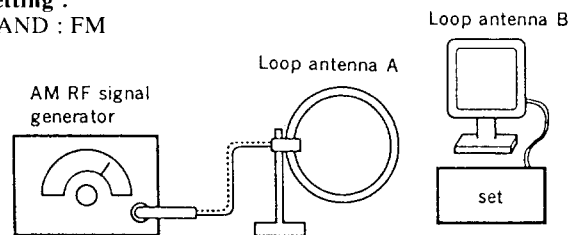
L-CH Stereo separation : Ⓐ - Ⓑ[Ⓐ]
R-CH Stereo separation : Ⓒ - Ⓓ[Ⓒ]

The separations of both channels should be equal.

AM SECTION

AM Tuned Indication Lighting Level

Setting :
BAND : FM



Procedure :

1. Set loop antenna A so that the loop antenna B input level becomes 55dBμ/m (0.6m V/m).
2. Tune the set to 1050kHz (CND model) or 999kHz (Except CND model).
3. Adjust RV51 so that the TUNED indicator goes on.

SW Control Voltage Adjustment (Frequency Coverage Adjustment) (SP model)

Setting :

BAND : SW

Procedure :

1. Connect digital voltmeter to diode D1 center lead and ground.
2. Adjust for a following value reading on digital voltmeter.

	Set frequency	Adjustment part	Reading on digital voltmeter
SW	f min. 5.95MHz	T2	1.2Vdc
	f min. 17.9MHz	CV2	8.5Vdc

Note :

CND : Canadian model

IT : Italian model

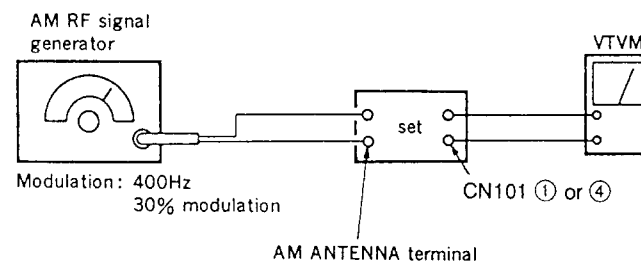
G : German model

SP : Singapore model

SW Tracking Adjustment Adjustment (SP model)

Setting :

BAND : SW



Procedure :

Adjust for a maximum reading on VTVM.

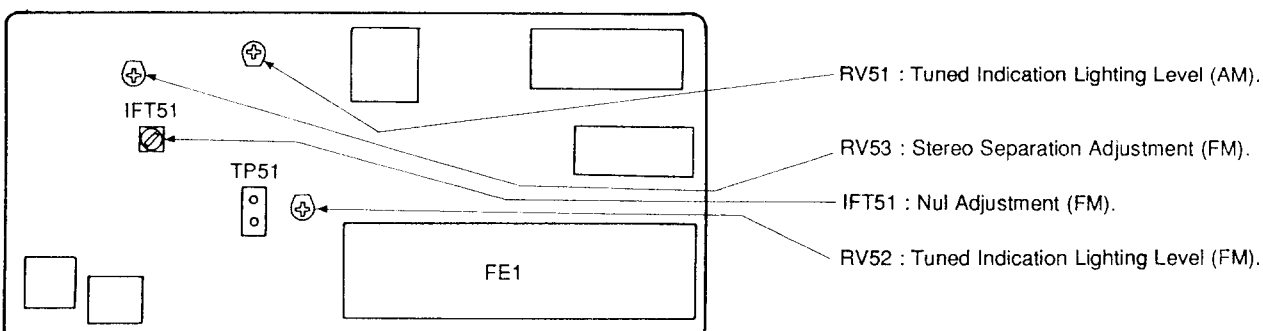
	Set frequency	Adjustment part
SW	f low 7MHz	T1
	f high 17MHz	CV1

- Repeat the procedures is each adjustment several times, and the frequency coverage and tracking adjustments should be finally done by the trimmer capacitors.

Adjustment Location:

TCB BOARD (AEP, G, IT model)

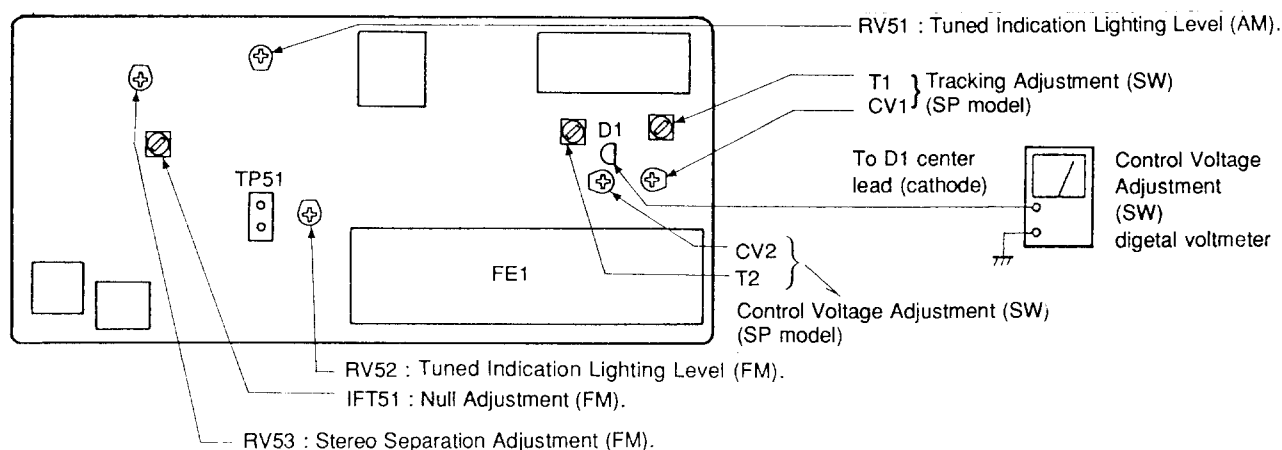
— Component Side —



Adjustment Location:

TCB BOARD (CND, SP model)

— Component Side —

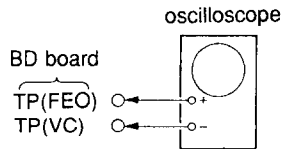


CD SECTION

Note :

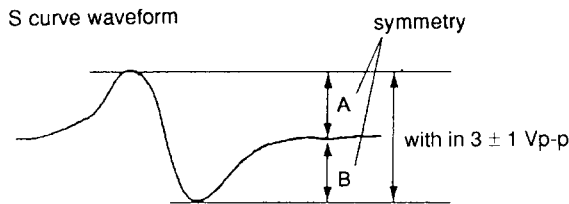
1. CD Block basically constructed to operate without adjustment. Therefore, check each item in order given.
2. Use YEDS-18 disc (3-702-101-01) unless otherwise indicated.
3. Use the oscilloscope with more than 10MΩ impedance.
4. Clean an object lens by an applicator with neutral detergent when the signal level is low than specified value with the following checks.

S Curve Check



Procedure :

1. Connect oscilloscope to test point TP (FEO) on BD board.
2. Connect between test point TP (FES : PIN ④⑦ of IC101) and TP (VC : PIN ① of IC101) by lead wire.
3. Turned Power switch on.
4. Put disc (YEDS-18) in and turned Power switch on again and actuate the focus search. (actuate the focus search when disc table is moving in and out.)
5. Check the oscilloscope waveform (S-curve) is symmetrical between A and B. And confirm peak to peak level within 3 ± 1 Vp-p.

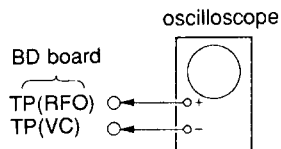


6. After check, remove the lead wire connected in step 2.

Note :

- Try to measure several times to make sure than the ratio of A : B or B : A is more than 10 : 7.
- Take sweep time as long as possible and light up the brightness to obtain best waveform.

RF Level Check

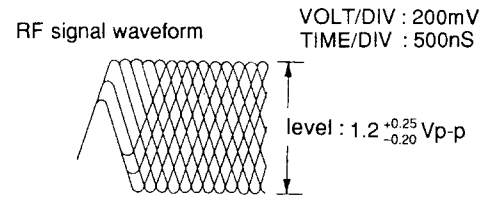


Procedure :

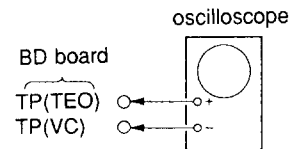
1. Connect oscilloscope to test point TP (RFO : PIN ④⑩ of IC101) on BD board.
2. Turned Power switch on.
3. Put disc (YEDS-18) in and playback.
4. Confirm that oscilloscope waveform is clear and check RF signal level is correct or not.

Note :

Clear RF signal waveform means that the shape “◇” can be clearly distinguished at the center of the waveform.

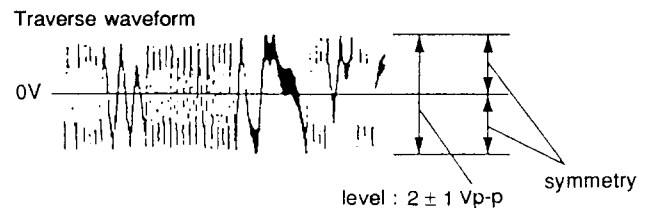


E-F Balance Check



Procedure :

1. Connect test point TP (ADJ : PIN ①⑨ of IC202) to ground and TP(TES : PIN ④③ of IC101) to TP (VC) with lead wire.
2. Connect oscilloscope to test point TP (TEO) on BD board.
3. Turned Power switch on.
4. Put disc (YEDS-18) in and playback.
5. Confirm that the oscilloscope waveform is symmetrical on the top and bottom in relation to 0Vdc, and check this level.

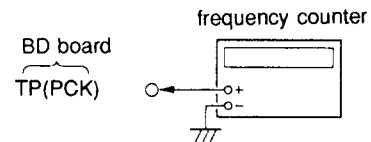


6. Remove the lead wire connected in step 1.

RF Free-run Frequency Check

Procedure :

1. Connect frequency counter to test point (PCK : PIN ④① of IC201) with lead wire.



2. Turned Power switch on.
3. Put disc (YEDS-18) in and playback.
4. Confirm that reading on frequency counter is 4.3218MHz.

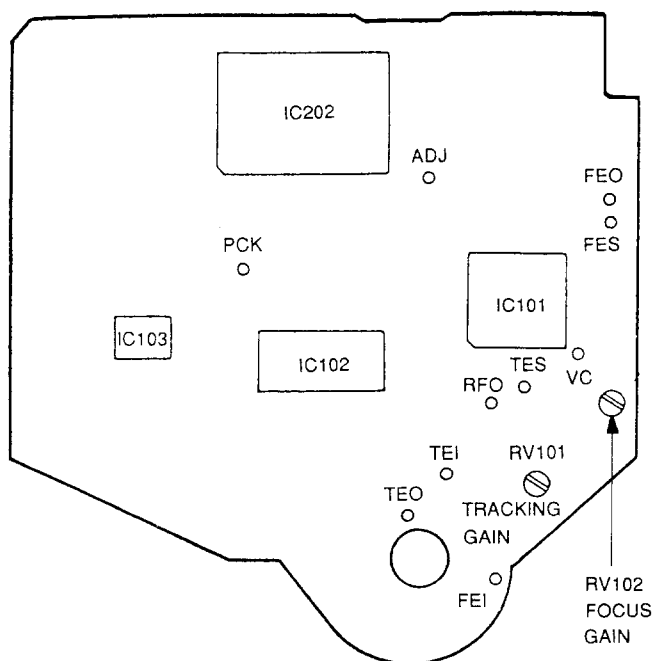
Focus/Tracking Gain

This gain has a margin, so even if it is slightly off. There is no problem.

Therefore, do not perform, this adjustment (RV101, RV102). Please note that is should be fixed to mechanical center position when you moved and do not know original position.

Adjustment Location :

[BD BOARD] — Conductor Side —



TEST MODE

1. Perform key check, FL tube check, equalizer check, and memory reset test modes as follows. These test modes can be exited by pressing the **SYSTEM POWER** key.

(1) Key and FL Tube Check

While pressing the **FUNCTION** and **TIMER 2** keys, press the **SYSTEM POWER** key. the test mode will be set

All FL tubes light up

Press the **SYSTEM POWER** key

The test will end.

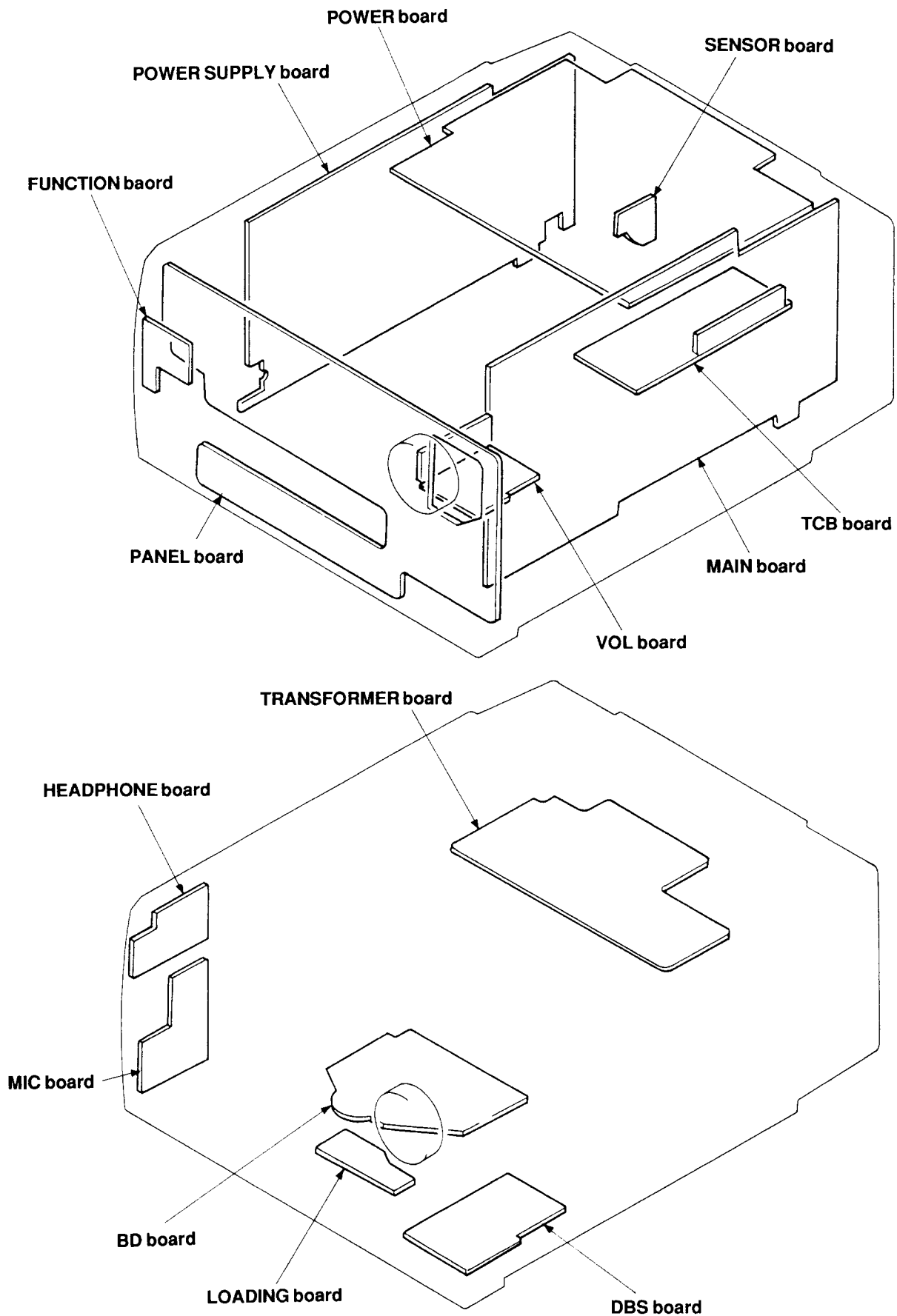
(2) Forced Cold Start

With the power cord disconnected and **SYSTEM POWER** key presses, connect the power supply cord. The internal RAM and non-volatile memory will be reset.

Note : The above will erase the preset memory and return the unit to the initial state. Write down the preset data before performing the test, and be sure to set this data after the test.

SECTION 5 DIAGRAMS

5-1. CIRCUIT BOARDS LOCATION





5-2. IC PIN FUNCTIONS

• IC202 CD System Controller (μ PD75116GF-J06-3BE)

Functions effected by the captioned controller include IC101 (RF signal processing, servo), IC201 (DSP, CLV SERVO), IC203 (Digital filter) and loading control in the CD unit, data exchange with IC301 (System controller), audio bus entry, etc.

Pin No.	Pin Name	I/O	Function
1, 2			Not in use with the model (open).
3	DPCLK	O	Display data transfer clock output to IC301 (system controller).
4	LATCH	O	Latch output to IC203 (Digital filter).
5	SHIFT	O	Shift output to IC203 (Digital filter).
6	ATT	O	Attenuation output to IC203 (Digital filter).
7	RESET	I	System reset input. "L": Reset.
8	X2	O	Clock output.
9	X1	I	Clock input (4 MHz).
10	DFCT SW	O	DEFECT circuit ON/OFF switching output of IC101 (CXA1372AQ). It is turned OFF ("H") to focus-search the DISC flaw detection circuit.
11	AMUTE	O	Muting control output. "H": Mute.
12	BSOUT	O	Audio bus output.
13	AFADJ	I	Test mode input. Various test operations are effected upon "L" after turning ON the POWER.
14	LDON	O	Optical pickup laser diode ON/OFF switching output. "H": ON.
15	XLT	O	Serial data latch output to IC201 (CXD2500BQ).
16	CLK	O	Serial data transfer clock output to IC201 (CXD2500BQ).
17	DATA	O	Serial data output to IC201 (CXD2500BQ).
18	MODE	I	Not in use with the model (pull up).
19	ADJ	I	Test mode input. Upon "L", GFS checking is disabled while continuously rotating the spindle no matter whether frame synch is issued during PLAY, PAUSE or SEARCH.
20	GFS	I	GFS signal input from IC201 (CXD2500BQ). "L": NG "H": OK.
21	FOK	I	Focus OK signal input from IC101 (CXA1372AQ). "H": OK.
22, 23		O	Not in use with the model (open).
24	LOD OUT	O	Output to rotate M291 (loading motor) in the loading out direction. *1.
25	LOD IN	O	Output to rotate M291 (loading motor) in the loading in direction. *1.
26	Vss	—	Power terminal (GND).
27	IN SW	I	S292 (Loading in switch) input.
28	OUT SW	I	S291 (Loading out switch) input.
29	KEYREQ	I	Key data request input from IC301 (system controller).
30	BSIN	I	Audio bus input.
31 to 36			Not in use with the model (GND).
37	SENSE	I	SENS input from IC201 (CXD2500BQ).
38	TIMER	I	Not in use with the model (pull up).
39	D/F 16BIT	I	IC201 (CXD2500BQ) digital filter mode setting input. It is fixed at 16 bit, 4fs with the model (pull up).
40			Not in use with the model (GND).
41	SUBQ	I	Subcode Q data input from IC201 (CXD2500BQ).
42		O	Not in use with the model (open).
43	SQCLK	O	Subcode Q data readout clock output to IC201 (CXD2500BQ).
44	SCOR	I	Subcode sync S0 + S1 detection input from IC201 (CXD2500BQ).
45 to 56		O	Not in use with the model (open).
57	NC	I	Not in use with the model (+5V).
58	VDD	—	Power terminal (+5V).
59 to 62	DPDATA3 to 0	I/O	Key data input and display data output with IC301 (system controller).
63, 64			Not in use with the model (open).

*1 Loading motor control

	IN	OUT	BRAKE
LOD OUT 	L	H	H
LOD IN 	H	L	H

• **IC301 Main System Controller (μ PD78012BGC-607-AB8)**

The terminals work to control tuner section (IC21, 51), FL tube display, spectrum analyzer, according to key input, and data exchange with IC202 (CD system controller).

Pin No.	Pin Name	I/O	Function
1	D0	I/O	Display data input and key data output for IC202 (CD system controller).
2	D1	I/O	
3	D2	I/O	
4	D3	I/O	
5	KEY REQ	O	Key data request output to IC202 (CD system controller).
6	AU-BUS IN	I	Audio bus input.
7	AU-BUS OUT	O	Audio bus output.
8	SIRCS	I	Remote commander signal (SIRCS) input.
9	Vss	—	Power terminal (GND).
10	POWER ON/OFF	O	Power ON/OFF output. "L": ON, "H": OFF.
11	MUTE	O	Muting control output. "L": Mute.
12	FUNC A	O	Input selection output to IC121 (Input selector).
13	FUNC B	O	
14	EQ A	O	Surround ON/OFF output.
15	EQ B	O	Karaoke pon ON/OFF output.
16	MONO	O	Not in use with the model.
17	AM/FM	O	AM (MW)/FM selection output. "L": FM, "H": AM.
18	IFOK	O	IF count OK output. "L": OK, "H": NG.
19	STEREO	I	FM stereo discriminating signal from tuner section.
20	TUNED	I	Tuning discriminating signal from tuner section.
21	DATA IN	I	Serial data input from IC21 (PLL).
22	DATA	O	Serial data output to IC21 (PLL), IC130 (Graphic equalizer).
23	CLOCK	O	Serial clock output to IC21 (PLL), IC130 (Graphic equalizer).
24	Vss	—	Power terminal (GND).
25	CE	O	Chip enable output to IC21 (PLL).
26	LATCH	O	Latch output to IC130 (Graphic equalizer).
27	TUNING	O	Tuning LED drive output.
28	PRESET	O	Preset LED drive output.
29	FUNC INH	O	Selection inhibit output to IC121 (Input selector).
30	CD ON	O	CD ON/OFF output.
31		—	Not in use with the model.
32		—	
33	VOL-	O	Volume motor control output.
34	VOL+	O	
35	RST	I	System reset input.
36	BACK UP	I	Back-up mode detecting.
37	DPCLK	I	CD display data timing input from IC202 (CD system controller).
38	CLOCK	O	Serial clock output to IC302 (Memory).
39	DATA	I/O	Writing data to/reading from IC302 (Memory).
40	VDD	—	Power terminal (+5V).

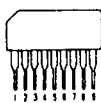
Pin No.	Pin Name	I/O	Function
41	X2	O	Main system clock 8.38 MHz.
42	X1	I	
43	GND	—	Power terminal (GND).
44	XT2	O	Sub system clock 32.768 kHz.
45	XT1	I	
46	AVss	—	Power terminal (GND).
47	ANI0	I	Key input. *1
48	ANI1	I	
49	ANI2	I	
50	ANI3	I	
51	SPEANA IN	I	Spectrum analyzing input (1 kHz, 2.5 kHz, 6.3 kHz, 16 kHz).
52	SPEANA IN	I	Spectrum analyzing input (63 Hz, 160 Hz, 400 Hz).
53	VOL POSITION	I	Volume control position detecting.
54	ANI7		
55	+5V	—	(+5V).
56	+5V	—	
57		—	Not in use with the model.
58	DATA	O	Serial data output to IC307 (FL driver).
59	CLOCK	O	Serial clock output to IC307, 308 (FL driver).
60	LATCH	O	Latch output to IC307, 308 (FL driver).
61	CL	O	Clear output to IC307, 308 (FL driver).
62	INH	O	Spectrum analyzing inhibit output.
63	B	O	Spectrum analyzing selection output.
64	A	O	

*1

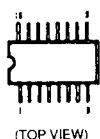
Voltage (Vdc)	0.0	0.6	1.3	2.0	2.6	3.2	3.6	5.0
Pin ④⑦	TUNING MODE	—	BAND	+	EQ PRESET	FUNCTION		No Input
Pin ④⑧	SYSTEM POWER ON/STANDBY	▷□□	□	▷▷	◁◁	OPEN/CLOSE	DIRECT	No Input
Pin ④⑨	TIMER1	TIMER REC	PLAY MODE	▷◁	◁◁	STEREO/MONO		No Input
Pin ⑤⑩	TIMER2	MEMORY/NEXT	KARAOKE PON					No Input

5-12. SEMICONDUCTOR LEAD LAYOUTS

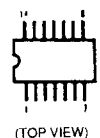
BA3308



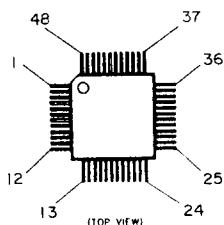
**CD4052BCM
CD4053BCM
TC9215F**



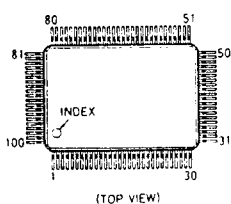
CD4066BCM



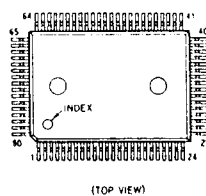
CXA1372AQ



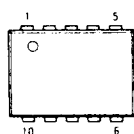
CXA1492AQ



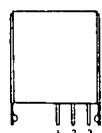
CXD2500BQ



LA5601

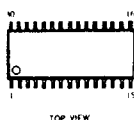


**GP1F32T
GP1U90XB**



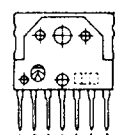
1 VDD
2 VCC
3 GND

**LA1851NM
LA6525M**

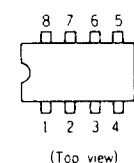


TOP VIEW

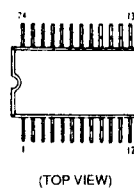
**LA5667
M5218AL**



LB1639



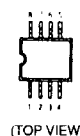
**LC7218M
MSM6538-01GS-VKR1**



LM35DZ

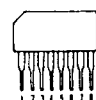


**M51953BFP
M5218AFP
μPC4558G2
X24C01S**

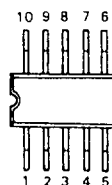


(TOP VIEW)

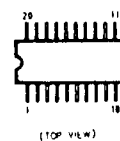
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M5230L-A
μPC1237HA**



M54641FP

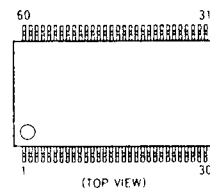


PCM-67U-B



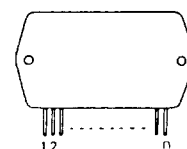
(TOP VIEW)

SN755703FT

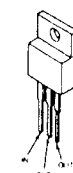


(TOP VIEW)

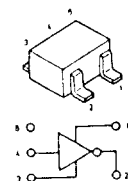
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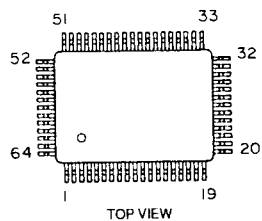
TA7820S



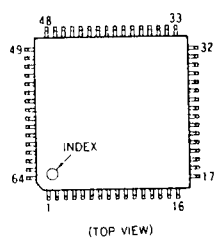
TC7SU04F



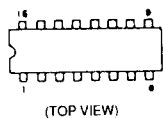
μPD75116GF-J06-3BE



μPD78012BGC-607-AB8



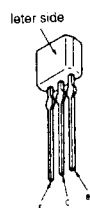
XR1091DCP



DTA124ES
DTC114ES
DTC124ES
DTC144ES
2SC2603-EF



2SA1175-HFE



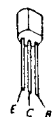
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2SA1678
2SC2814-F4
2SC3395
2SC3396
2SC4154-F
2SC4398
2SC4666B



2SB1094-LK
2SD2012



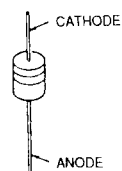
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2SC1841-PAFAEA
2SD1616A-K



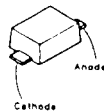
2SK208-GR3



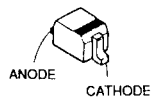
HZS6A1L
11ES2
UZL-33H



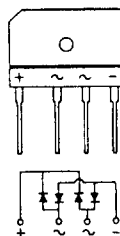
MA8039



MA8056
1SS352



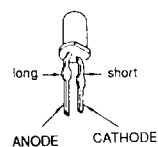
RBA-402



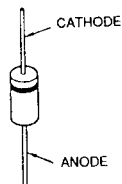
RD5.6M-B2
UZM5.1B



SEL5220S



UZL-6M2
1N4148M

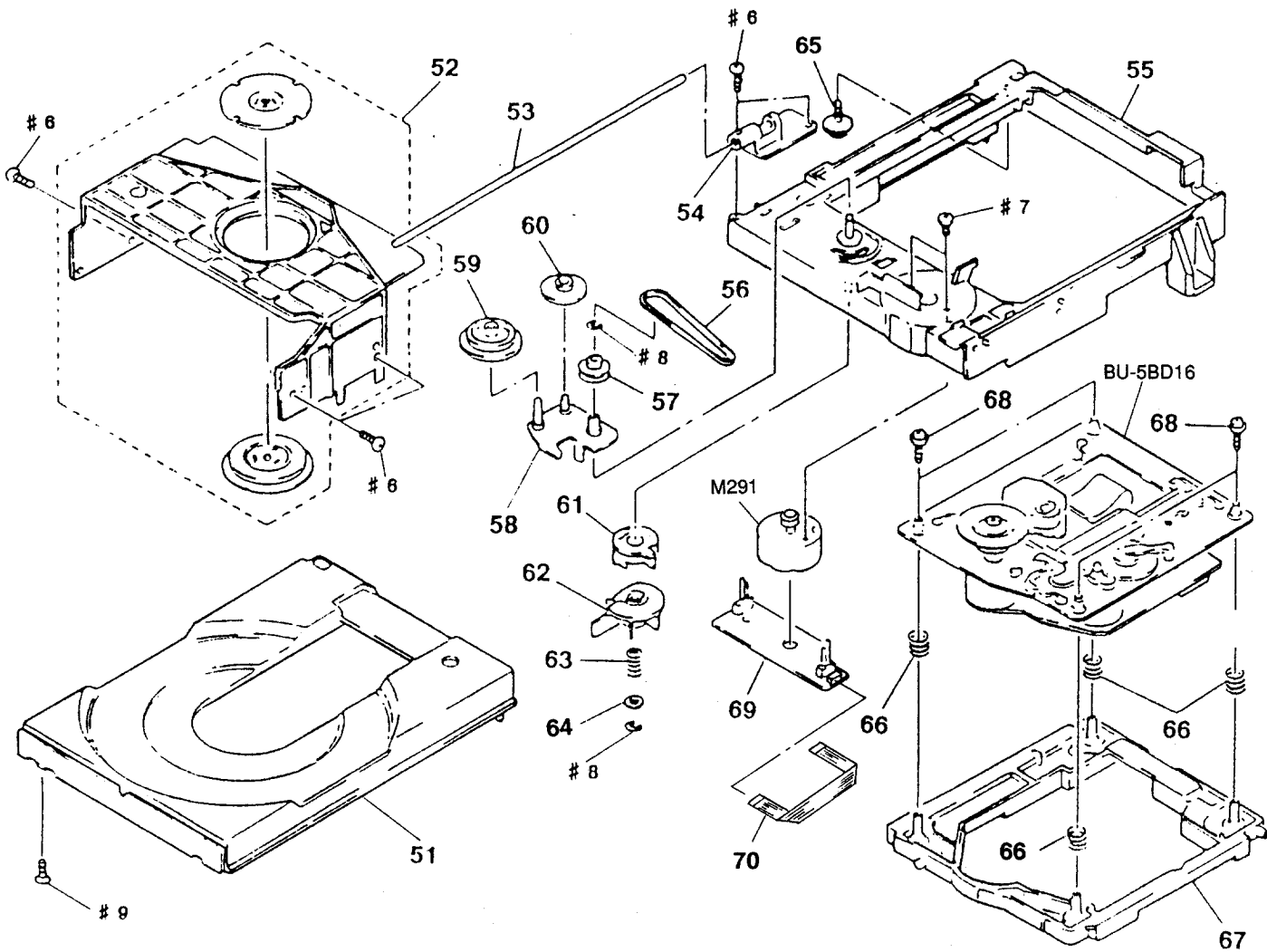


1SS226



Exploded view diagram of the CDM13BC-5BD16 LED lighting fixture assembly. The diagram shows the main housing (10), lens (18), and various internal components like the LED module (14), driver (15), and heat sink (16). Numerous screws and fasteners are labeled with quantities (e.g., #4, #3, #2). Key components are labeled with part numbers: F903, F904, F901, T900, FL301, RV361, CNJ100, M901, and CDM13BC-5BD16. Some parts are marked 'not supplied'.

6-2. CD MECHANISM SECTION (CDM13BC-5BD16)



**6-3. BASE UNIT SECTION
(BU-5BD16)**

